	and the second s	ń
Reg. No. :		

Question Paper Code: P 1292

B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2009.

Fifth Semester

Electrical and Electronics Engineering

EE 1301 - POWER ELECTRONICS

(Common to Electronics and Instrumentation Engineering/Instrumentation and Control Engineering)

(Common to B.E. (Part-Time) Fourth Semester-Regulation 2005)

(Regulation 2004)

Time: Three hours

Maximum: 100 marks

Answer ALL questic

PART A — $(10 \times 2 = 20 \text{ marks})$

- 1. What is holding current of SCR?
- 2. Define the pinch off voltage off MOSI E1.
- 3. Why power factor of semi converter is better than full converter?
- 4. What are the basic requirements for the successful firing of the thyristor?
- 5. What are the disadvantages of a squency modulation scheme over the pulse width modulation scheme?
- 6. What do you mean by intoral vele control?
- 7. Mention the methods av. ila de for the output voltage control of inverters.
- 8. List the different types of PWM techniques.
- 9. What is on-line UPs.
- 10. Mention the different types of HVDC link.

PART B — $(5 \times 16 = 80 \text{ marks})$

 (a) Discuss steady state and the switching characteristics of power MOSTET.

Or

(b) Sketch the transverse and switching characteristics of IGBT.

 (a) Explain the operation of three phase fully controlled rectifier supplying R load with neat waveforms and also derive an expression for average output voltage.

Or

- (b) A single phase full converter is supplied from 230 V, 50 Hz source. The load consists of R = 10 Ω and a large inductance so as to render the load current constant. For a firing angle of 30 degree, determine
 - (i) average output voltage
 - (ii) average output current
 - (iii) average and rms values of thyristor currents
 - (iv) the power factor.

Also if the source has an inductance of 1.5 mF, determine

- (1) average output voltage
- (2) the angle overlap
- (3) the power factor.
- (a) Classify the basic topologies of switting regulators and explain the operation of cuk converter.

Oi

- (b) Describe a ZVS resonant of werter with appropriate circuits and waveforms.
- (a) Discuss the function of three phase voltage source inverter supplying a balanced star connected load in 180 degree operating mode.

Or

- (b) Explain the operation of single phase capacitor commutated CSI with R load.
- 15. (a) Explain : operation of on-line and off-line UPS in detail.

Or

(b) Discuss the principle of operation of unified power controller as pensator with a neat circuit arrangement.